

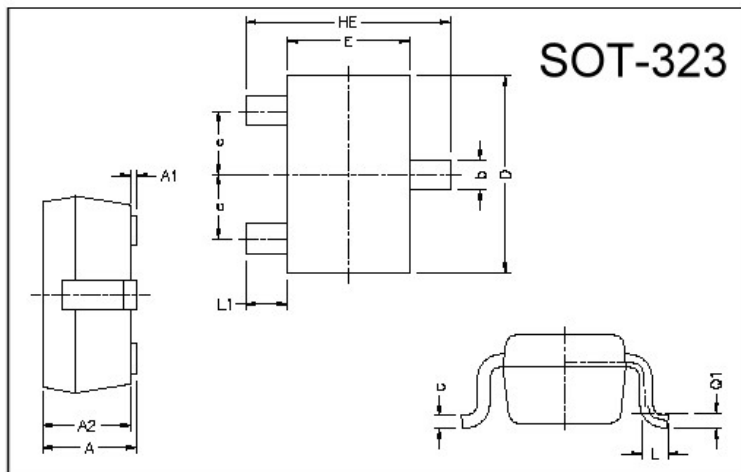
GSMBTA56

PNP SILICON TRANSISTOR

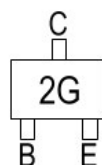
Description

The GSMBTA56 is designed for general purpose amplifier applications.

Package Dimensions



Marking :



REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	0.80	1.10	L1	0.42 REF.	
A1	0	0.10	L	0.15	0.35
A2	0.80	1.00	b	0.25	0.40
D	1.80	2.20	c	0.10	0.25
E	1.15	1.35	e	0.65 REF.	
HE	1.80	2.40	Q1	0.15 BSC.	

Absolute Maximum Ratings at Ta = 25°C

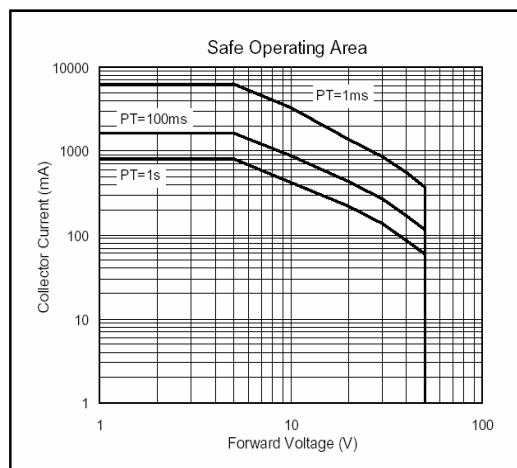
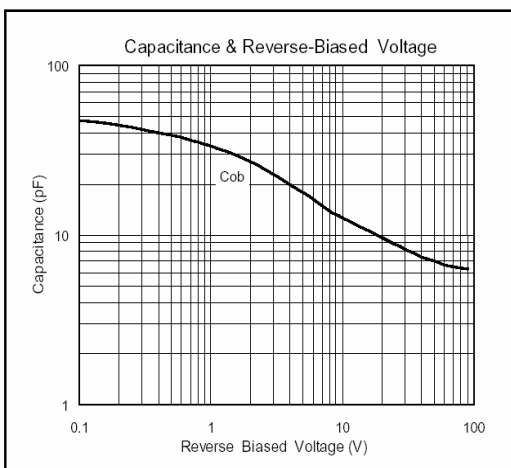
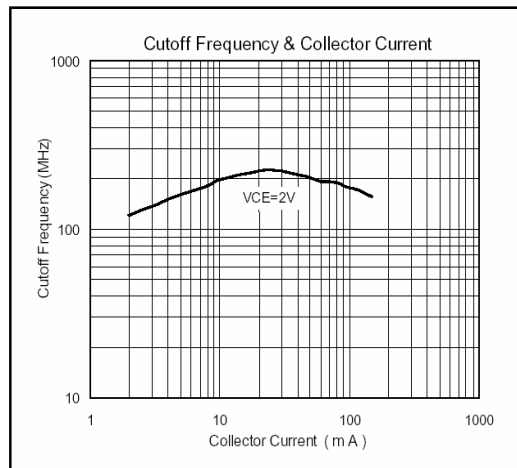
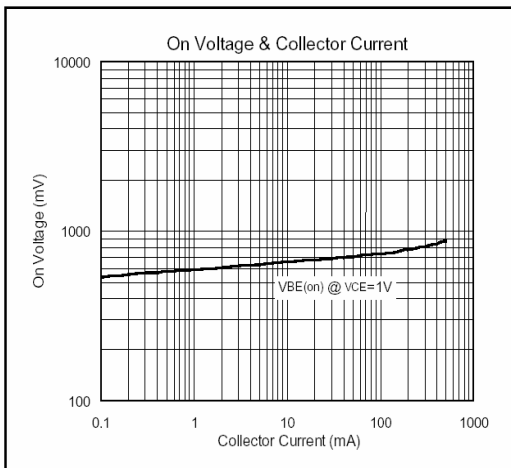
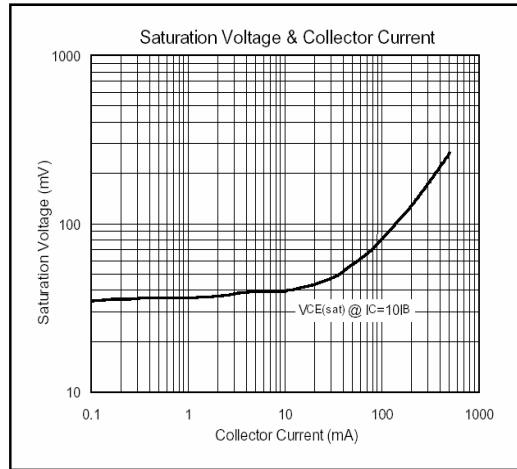
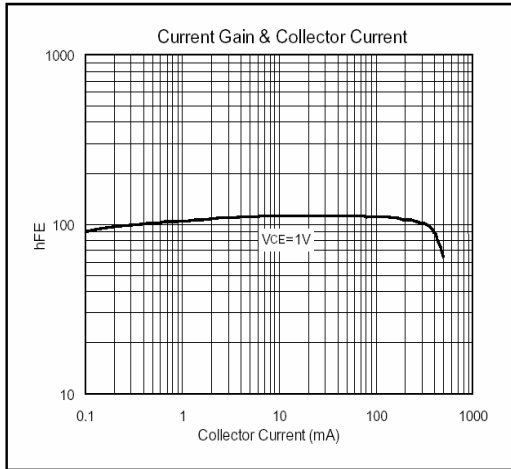
Parameter	Symbol	Ratings	Unit
Junction Temperature	T _j	+150	°C
Storage Temperature	T _{stg}	-55~+150	°C
Collector to Base Voltage	V _{CB0}	-80	V
Collector to Emitter Voltage	V _{CEO}	-80	V
Emitter to Base Voltage	V _{EBO}	-4	V
Collector Current	I _c	-500	mA
Total Power Dissipation	P _d	225	mW

Electrical Characteristics (Ta = 25°C, unless otherwise noted)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
BV _{CB0}	-80	-	-	V	I _c =-100uA, I _E =0
BV _{CEO}	-80	-	-	V	I _c =-1mA, I _B =0
BV _{EBO}	-4	-	-	V	I _E =-100uA, I _c =0
I _{CB0}	-	-	-100	nA	V _{CB} =-80V, I _E =0
I _{CEO}	-	-	-100	nA	V _{CE} =-60V, I _B =0
*V _{CE(sat)}	-	-	-250	mV	I _c =-100mA, I _B =-10mA
*V _{BE(on)}	-	-	-1.2	V	V _{CE} =-1V, I _c =-100mA
*h _{FE1}	50	-	-		V _{CE} =-1V, I _c =-10mA
*h _{FE2}	50	-	-		V _{CE} =-1V, I _c =-100mA
f _T	100	-	-	MHz	V _{CE} =-2V, I _c =-10mA, f=100MHz

* Pulse Test: Pulse Width ≤ 380μs, Duty Cycle ≤ 2%

Characteristics Curve



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