

GSS4936

N-CHANNEL ENHANCEMENT MODE POWER MOSFET

BVDSS	25V
RDS(ON)	37mΩ
ID	5.8A

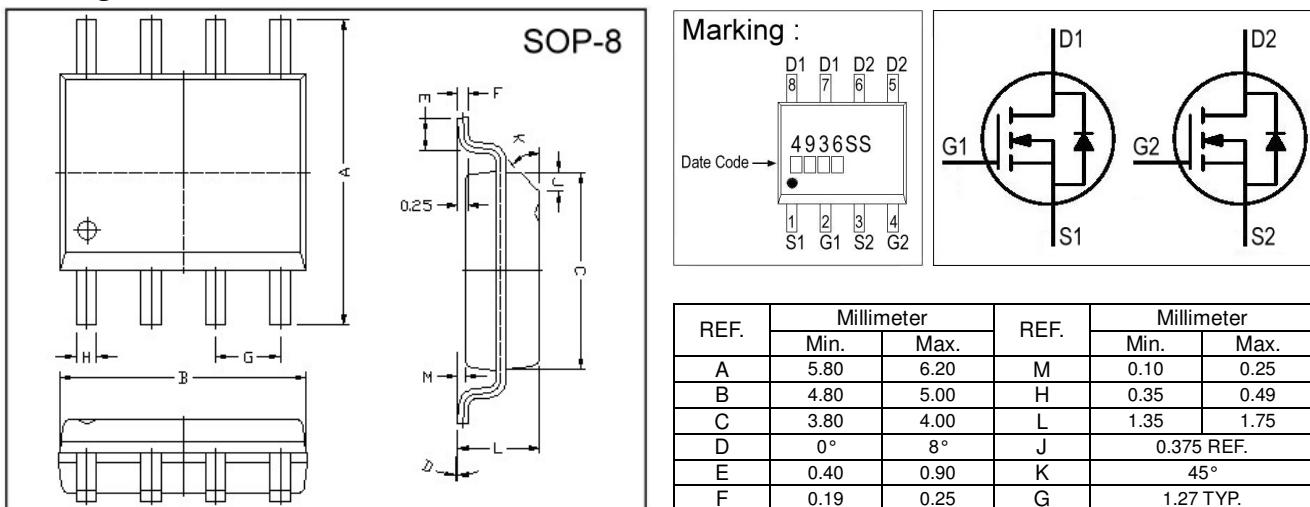
Description

The GSS4936 provide the designer with the best combination of fast switching, ruggedized device design, ultra low on-resistance and cost-effectiveness.

Features

- *Simple Drive Requirement
- *Low Gate Charge
- *Fast Switching

Package Dimensions



Absolute Maximum Ratings

Parameter	Symbol	Ratings	Unit
Drain-Source Voltage	V _{DS}	25	V
Gate-Source Voltage	V _{GS}	±20	V
Continuous Drain Current ³	I _D @ TA=25°C	5.8	A
Continuous Drain Current ³	I _D @ TA=70°C	4.6	A
Pulsed Drain Current ¹	I _{DM}	30	A
Total Power Dissipation	P _D @ TA=25°C	2	W
Linear Derating Factor		0.016	W/°C
Operating Junction and Storage Temperature Range	T _j , T _{stg}	-55 ~ +150	°C

Thermal Data

Parameter	Symbol	Value	Unit
Thermal Resistance Junction-ambient ³ Max.	R _{thj-amb}	62.5	°C/W

Electrical Characteristics ($T_j = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Drain-Source Breakdown Voltage	BV_{DSS}	25	-	-	V	$\text{V}_{\text{GS}}=0$, $\text{I}_D=250\mu\text{A}$
Breakdown Voltage Temperature Coefficient	$\Delta \text{BV}_{\text{DSS}} / \Delta T_j$	-	0.03	-	V/ $^\circ\text{C}$	Reference to 25°C , $\text{I}_D=1\text{mA}$
Gate Threshold Voltage	$\text{V}_{\text{GS}(\text{th})}$	1.0	-	3.0	V	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}$, $\text{I}_D=250\mu\text{A}$
Forward Transconductance	g_{fs}	-	6.5	-	S	$\text{V}_{\text{DS}}=10\text{V}$, $\text{I}_D=5\text{A}$
Gate-Source Leakage Current	I_{GSS}	-	-	± 100	nA	$\text{V}_{\text{GS}}= \pm 20\text{V}$
Drain-Source Leakage Current($T_j=25^\circ\text{C}$)	I_{DSS}	-	-	1	μA	$\text{V}_{\text{DS}}=25\text{V}$, $\text{V}_{\text{GS}}=0$
Drain-Source Leakage Current($T_j=70^\circ\text{C}$)		-	-	25	μA	$\text{V}_{\text{DS}}=20\text{V}$, $\text{V}_{\text{GS}}=0$
Static Drain-Source On-Resistance ²	$\text{R}_{\text{DS}(\text{ON})}$	-	-	37	$\text{m}\Omega$	$\text{V}_{\text{GS}}=10\text{V}$, $\text{I}_D=5\text{A}$
		-	-	60		$\text{V}_{\text{GS}}=4.5\text{V}$, $\text{I}_D=3.5\text{A}$
Total Gate Charge ²	Q_g	-	6.9	-	nC	$\text{I}_D=5\text{A}$ $\text{V}_{\text{DS}}=16\text{V}$ $\text{V}_{\text{GS}}=5\text{V}$
Gate-Source Charge	Q_{gs}	-	1.2	-		
Gate-Drain ("Miller") Change	Q_{gd}	-	4.5	-		
Turn-on Delay Time ²	$\text{T}_{\text{d}(\text{on})}$	-	6	-	ns	$\text{V}_{\text{DS}}=16\text{V}$ $\text{I}_D=5\text{A}$ $\text{V}_{\text{GS}}=10\text{V}$ $\text{R}_G=3.3\Omega$ $\text{R}_D=3.2\Omega$
Rise Time	T_r	-	17.5	-		
Turn-off Delay Time	$\text{T}_{\text{d}(\text{off})}$	-	14.5	-		
Fall Time	T_f	-	5.5	-		
Input Capacitance	C_{iss}	-	218	-	pF	$\text{V}_{\text{GS}}=0\text{V}$ $\text{V}_{\text{DS}}=25\text{V}$ $f=1.0\text{MHz}$
Output Capacitance	C_{oss}	-	155	-		
Reverse Transfer Capacitance	C_{rss}	-	63	-		

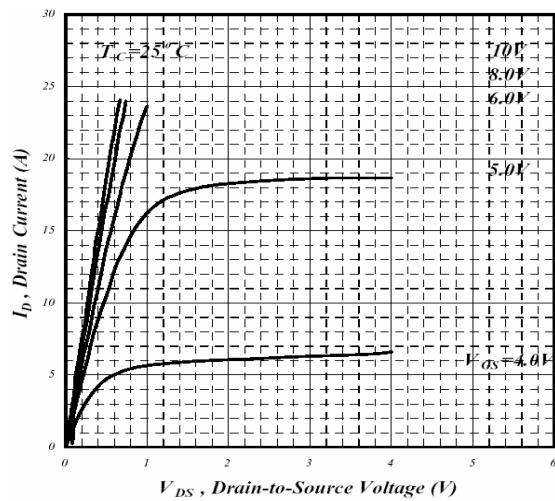
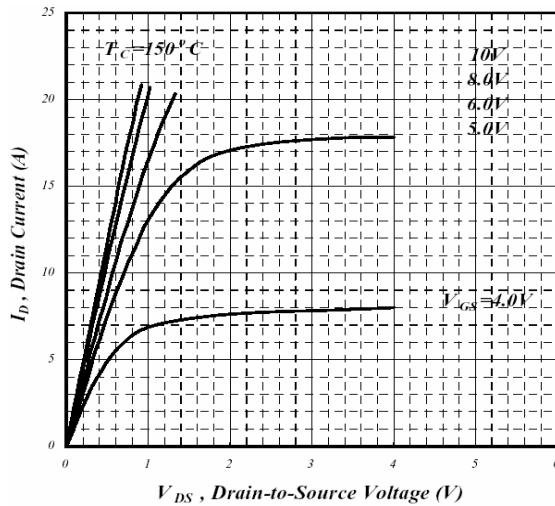
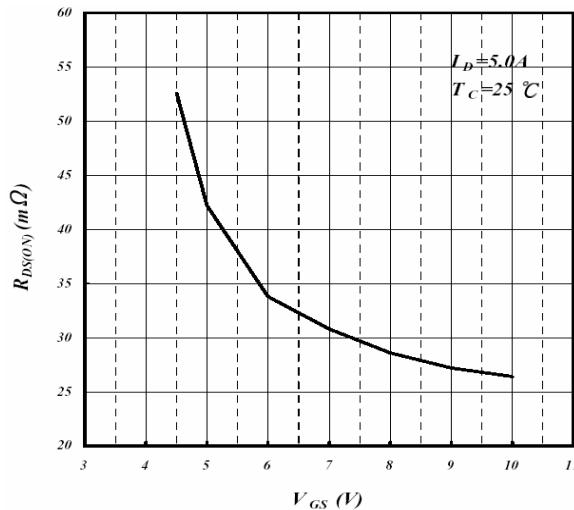
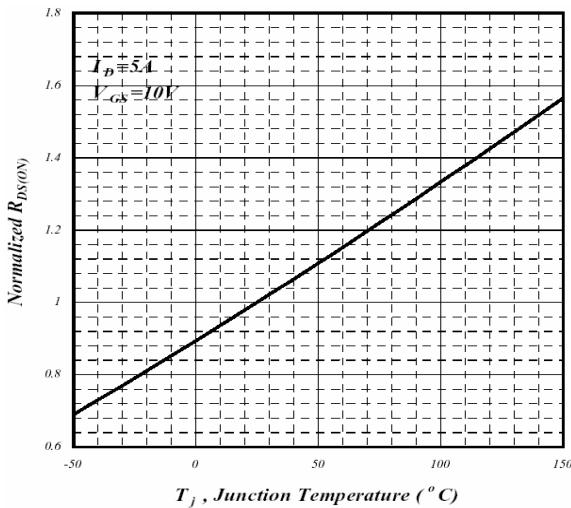
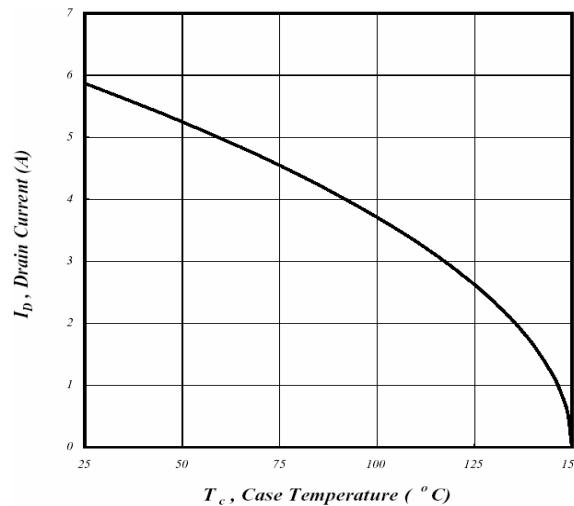
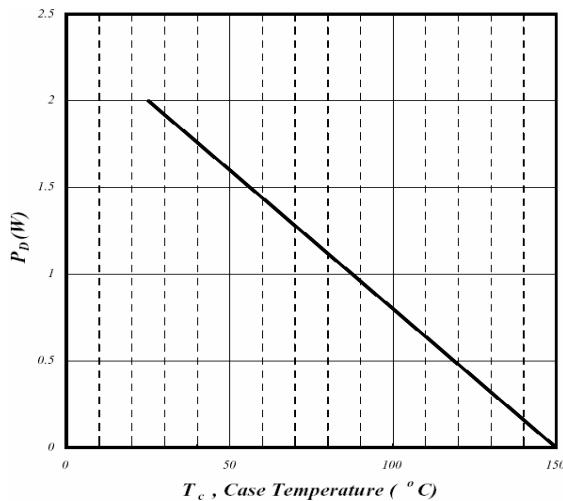
Source-Drain Diode

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Forward On Voltage ²	V_{SD}	-	-	1.2	V	$\text{I}_S=1.7\text{A}$, $\text{V}_{\text{GS}}=0\text{V}$, $T_j=25^\circ\text{C}$
Continuous Source Current (Body Diode)	I_S	-	-	1.67	A	$\text{V}_D=\text{V}_G=0\text{V}$, $\text{V}_S=1.2\text{V}$

Notes: 1. Pulse width limited by Max. junction temperature.

2. Pulse width $\leq 300\text{us}$, duty cycle $\leq 2\%$.

3. Surface mounted on 1 in² copper pad of FR4 board; $135^\circ\text{C}/\text{W}$ when mounted on Min. copper pad.

Characteristics Curve**Fig 1. Typical Output Characteristics****Fig 2. Typical Output Characteristics****Fig 3. On-Resistance v.s. Gate Voltage****Fig 4. Normalized On-Resistance v.s. Junction Temperature****Fig 5. Maximum Drain Current v.s. Case Temperature****Fig 6. Type Power Dissipation**

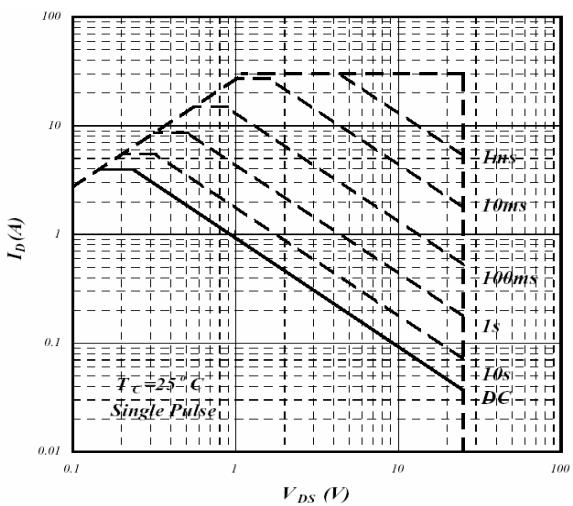


Fig 7. Maximum Safe Operating Area

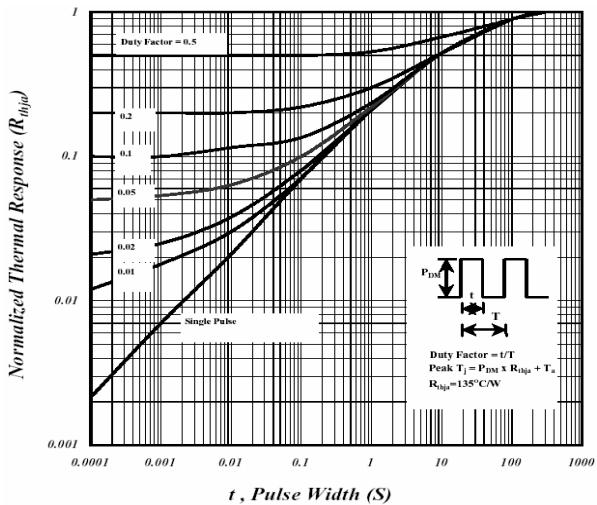


Fig 8. Effective Transient Thermal Impedance

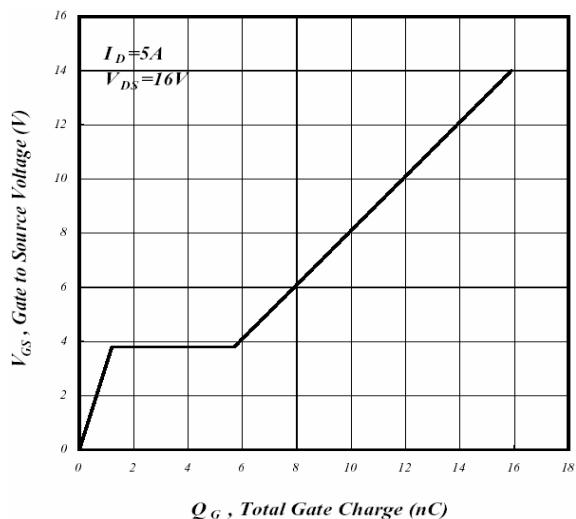


Fig 9. Gate Charge Characteristics

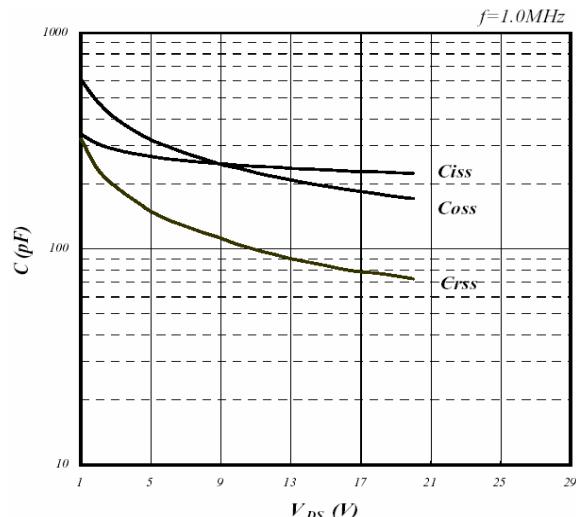


Fig 10. Typical Capacitance Characteristics

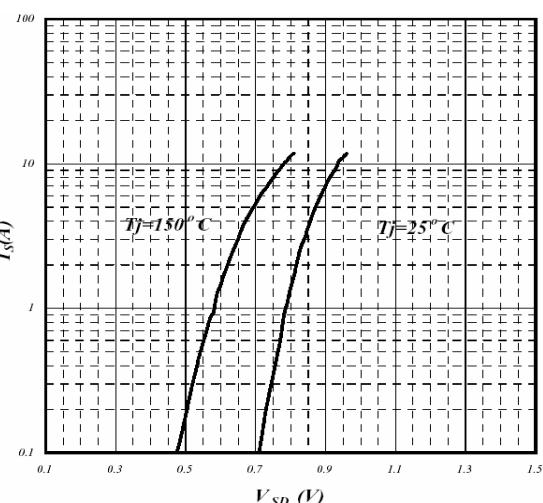


Fig 11. Forward Characteristics of Reverse Diode

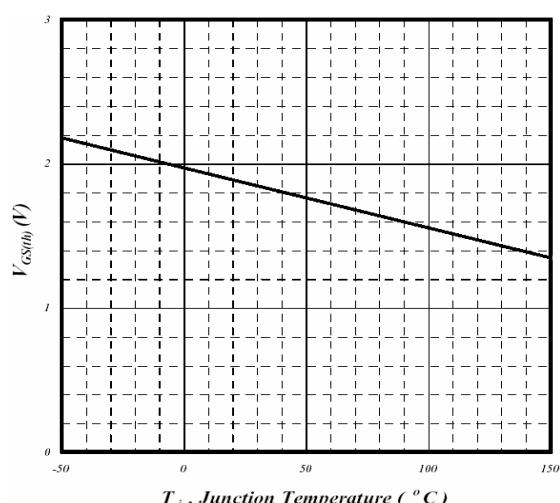


Fig 12. Gate Threshold Voltage v.s. Junction Temperature

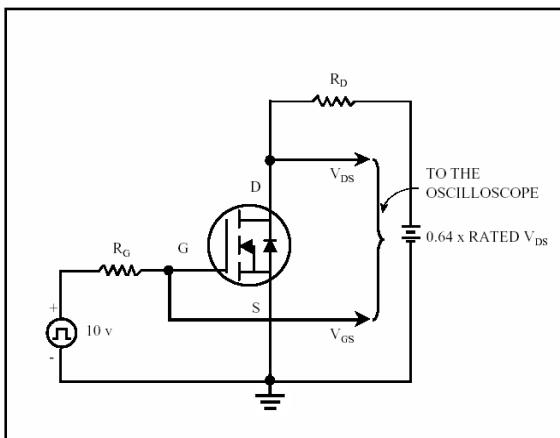


Fig 13. Switching Time Circuit

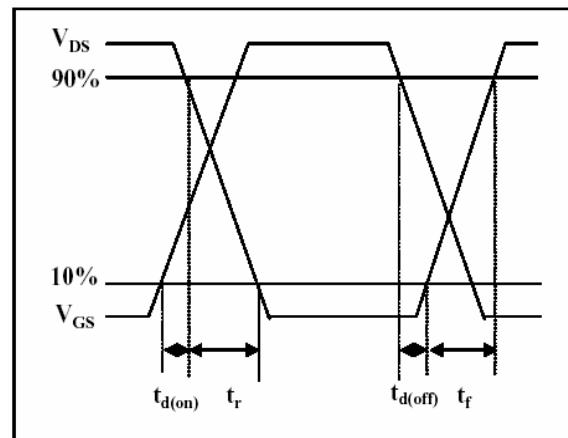


Fig 14. Switching Time Waveform

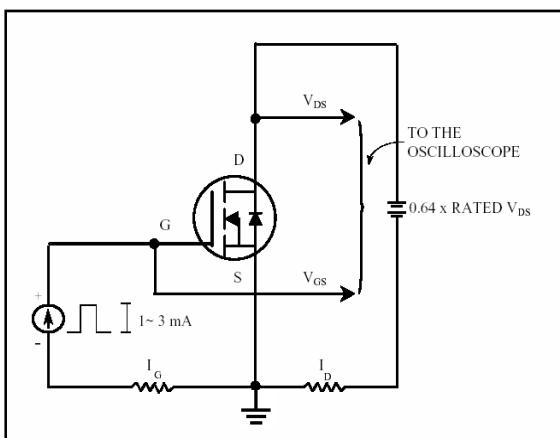


Fig 15. Gate Charge Circuit

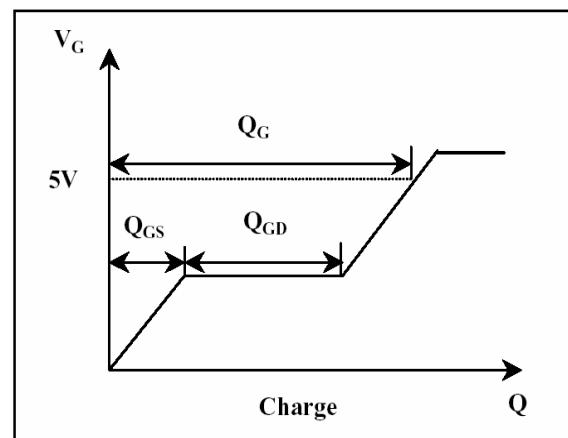


Fig 16. Gate Charge Waveform

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Head Office And Factory:

- **Taiwan:** No. 17-1 Tatung Rd. Fu Kou Hsin-Chu Industrial Park, Hsin-Chu, Taiwan, R. O. C.
TEL : 886-3-597-7061 FAX : 886-3-597-9220, 597-0785
- **China:** (201203) No.255, Jang-Jiang Tsai-Lueng RD. , Pu-Dung-Hsin District, Shang-Hai City, China
TEL : 86-21-5895-7671 ~ 4 FAX : 86-21-38950165