

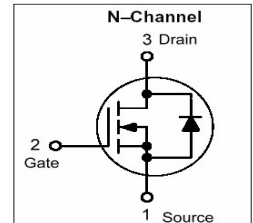
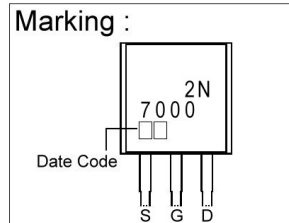
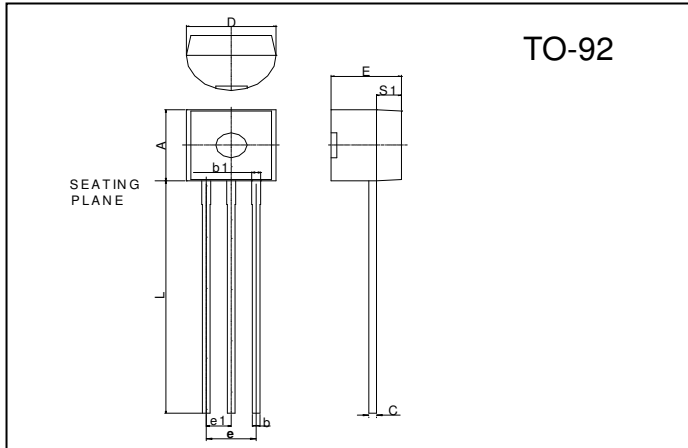
G2N7000

N-CHANNEL ENHANCEMENT MODE MOSFET

Description

The G2N7000 is designed for high voltage, high speed applications such as switching regulators, converters, solenoid and relay drivers.

Package Dimensions



REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	4.45	4.7	D	4.44	4.7
S1	1.02	-	E	3.30	3.81
b	0.36	0.51	L	12.70	-
b1	0.36	0.76	e1	1.150	1.390
C	0.36	0.51	e	2.42	2.66

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Ratings	Unit
Operating Junction and Storage Temperature Range	Tj, Tstg	-55 ~ +150	°C
Drain-Source Voltage	V _{DSS}	60	V
Gate-Source Voltage	V _{GS}	±20	V
-Continuous	V _{GSM}	±40	V
-Non-repetitive (tp ≤ 50us)			
Drain Current	I _D	200	mA
-Continuous	I _{DM}	500	
Power Dissipation	Ta=25°C	0.35	W
	Derate above 25°C	2.8	
Thermal Resistance ,Junction-to-Ambient	RθJA	357	°C/W
Maximum Lead Temperature for Soldering Purposes, 1/16" from case for 10 seconds	T _L	300	°C

Electrical Characteristics (Tc= 25°C unless otherwise noted)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Drain-Source Breakdown Voltage	V _{(BR)DSS}	60	-	-	V	V _{GS} =0, I _D =250uA
Gate Threshold Voltage	V _{GS(th)}	0.8	-	3.0	V	V _{DS} = V _{GS} , I _D =1.0mA
Gate Body Leakage Current	I _{GSS}	-	-	±100	nA	V _{GS} =±20V, V _{DS} =0
Zero Gate Voltage Drain Current	I _{DSS}	-	-	1	uA	V _{DS} =60V, V _{GS} =0
On-State Drain Current	I _{D(ON)}	75	-	-	mA	V _{GS} =4.5V, V _{DS} =10V
Static Drain-Source on-State Resistance	R _{DS(ON)}	-	-	5.0	Ω	V _{GS} =10V, I _D =500mA
		-	-	6.0		V _{GS} =4.5V, I _D =75mA
Drain-Source on-Voltage	V _{DS(ON)}	-	-	2.5	V	V _{GS} =10V, I _D =500mA
		-	-	0.45		V _{GS} =4.5V, I _D =75mA
Forward Transconductance	G _{FS}	100	-	-	mS	V _{DS} =10 V, I _D =200mA
Input Capacitance	C _{iss}	-	-	60	pF	V _{DS} =25V, V _{GS} =0V, f=1MHz
Output Capacitance	C _{oss}	-	-	25		
Reverse Transfer Capacitance	C _{rss}	-	-	5		

Switching Characteristics (Note 1)

Turn-on Delay Time	t_{on}	-	-	10	ns	$V_{DD}=15V, I_D=500mA$ $R_G=25\Omega, R_L=30\Omega, V_{gen}=10V$
Turn-off Delay Time	t_{off}	-	-	10		

Note 1. Pulse Test: Pulse Width $\leq 300\mu s$, Duty cycle $\leq 2\%$.

Characteristics Curve

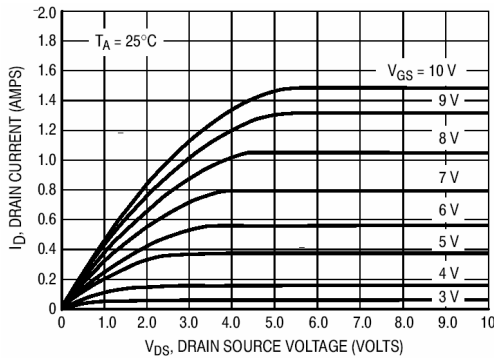


Figure 1. Ohmic Region

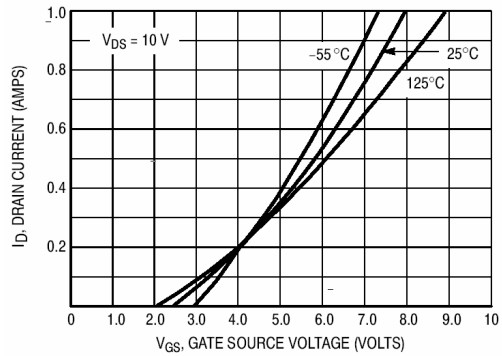


Figure 2. Transfer Characteristics

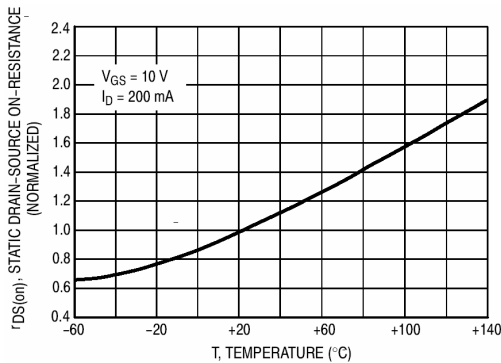


Figure 3. Temperature versus Static Drain-Source On-Resistance

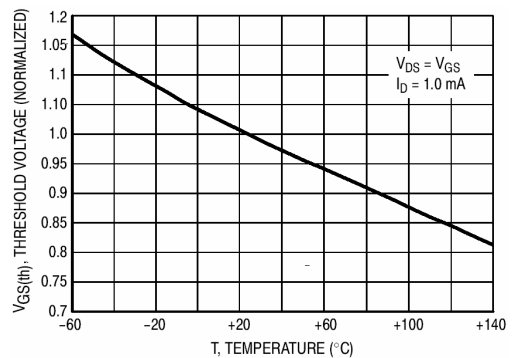


Figure 4. Temperature versus Gate Threshold Voltage

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