

G5M2163

CMOS Positive Voltage Regulator

Description

The G5M2163 series of positive, linear regulators feature low quiescent current (30 μ A typ.) with low dropout voltage, making them ideal for battery applications.

These rugged devices have both Thermal Shutdown, and Current Fold-back to prevent device failure under the "Worst" of operating conditions.

In applications requiring a low noise, regulated supply, place a 1000pF capacitor between Bypass and Ground. The G5M2163 is stable with an output capacitance of 2.2 μ F or greater.

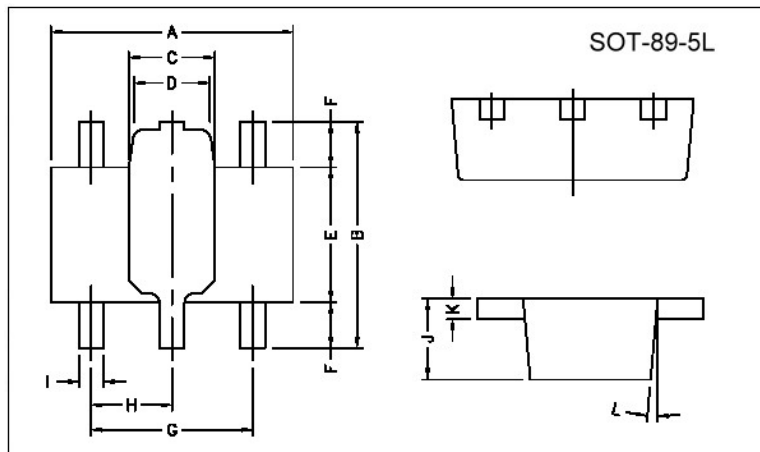
Features

- Very Low Dropout Voltage
- Guaranteed 300mA output
- Over-Temperature Shutdown
- Current Limiting
- Short Circuit Current Fold-back
- Highly Accurate $\pm 1.5\%$
- Noise Reduction Bypass Capacitor
- Power-saving Shutdown Mode
- Factor Pre-set Output Voltage

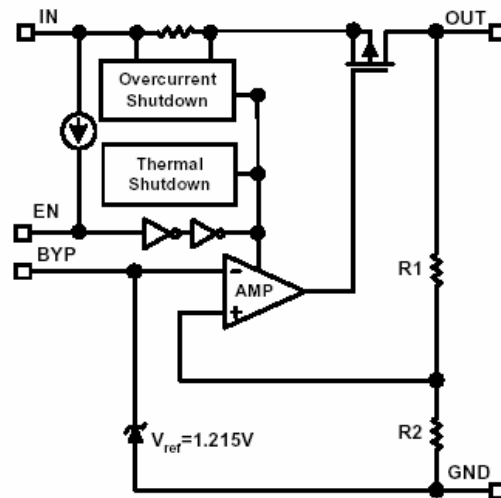
Applications

- Battery Powered Widgets
- Instrumentation
- Wireless Devices
- PC Peripherals
- Portable Electronics
- Cordless Phones
- Electronic Scales

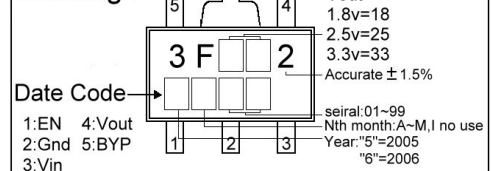
Package Dimensions



Functional Block Diagram

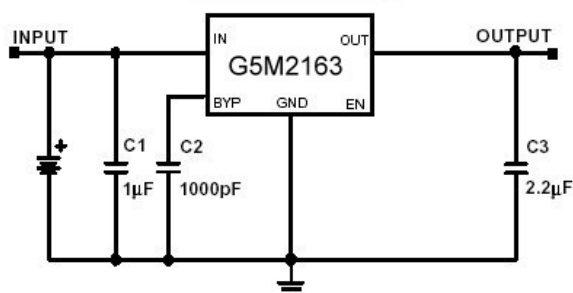


Marking :



REF.	Millimeter	REF.	Dimensions
	Min. Max.		Millimeter
A	4.40 4.60	G	3.00 REF.
B	4.05 4.25	H	1.50 REF.
C	1.50 1.70	I	0.40 0.52
D	1.30 1.50	J	1.40 1.60
E	2.40 2.60	K	0.35 0.41
F	0.80 -	L	5 ° TYP.

Typical Application Circuit



Absolute Maximum Ratings

Parameter	Symbol	Ratings	Unit
Input Max Voltage	V _{IN}	8	V
Output Current	I _{OUT}	P _D /(V _{IN} - V _O)	mA
Output Voltage	V _{OUT}	1.5~5.0	V
Operating Ambient Temperature	T _{opr}	-40 ~ +85	°C
Junction Temperature	T _j	-40 ~ +125	°C
Maximum Junction Temperature	T _{j Max}	150	°C
Thermal Resistance	θ _{jc}	38	°C/W
	θ _{ja}	180	°C/W
Power Dissipation(ΔT=100°C)	PD	550	mW
EDS Classification		B	

Electrical Characteristics Ta=25°C unless otherwise noted

Parameter	Symbol	Condition	Min	TYP	Max	Unit
Output Voltage	V _{OUT(E)} (Note1)	V _{IN} =V _{OUT(T)} +2V, I _O =1mA	-1.5%	V _{OUT(T)} (Note2)	1.5%	V
		V _{IN} =V _{OUT(T)} +2V, I _O =300mA	-2.5%		2.5%	V
Output Current	I _O	V _{IN} =V _{OUT(T)} +2V, V _{OUT} ≥V _{OUT(E)} *0.96	300	-	-	mA
Current Limit	I _{LIM}	V _{IN} =V _{OUT(T)} +2V, V _O >1.2V	300	450	-	mA
Load Regulation	REG _{LOAD}	V _{IN} =V _{OUT(T)} +2V, I _O =1mA to 300mA	-1	0.2	1	%
Dropout Voltage	V _{DROPOUT}	I _O =300mA V _O =V _{OUT(E)} -2%	1.2V≤V _{OUT(T)} ≤2.0V	-	1300	mV
			2.0V<V _{OUT(T)} ≤2.8V	-	400	
			2.8V<V _{OUT(T)}	-	300	
Quiescent Current	I _Q	V _{IN} =V _{OUT(T)} +1V, I _O =0mA	-	30	50	μA
Ground Pin Current	I _{GNND}	V _{IN} =V _{OUT(T)} +2V, I _O =1mA~300mA	-	35	-	μA
Line Regulation	REG _{LINE}	I _O =1mA V _{IN} =V _{OUT(T)} +1 to V _{OUT(T)} +2	1.2V≤V _{OUT(T)} ≤1.4V	-0.2	0.2	%
			1.4V<V _{OUT(T)} ≤2.0V	-0.15	0.15	
			2.0V<V _{OUT(T)} <4.0V	-0.1	0.02	
			4.0V≤V _{OUT(T)}	-0.4	0.2	
Input Voltage	V _{IN}		Note3	-	7	V
Over Temperature Shutdown	OTS		-	150	-	°C
Over Temperature Hysteresis	OTH		-	30	-	°C
Output Voltage Temperature Coefficient	TC		-	30	-	ppm/°C
Short Circuit Current(Note4)	I _{SC}	V _{IN} =V _{OUT(T)} +1V, V _{OUT} <0.8V	-	150	300	mA
Power Supply Rejection	PSRR	I _O =100mA C _O =2.2μF	f=100Hz	-	60	dB
			f=1kHz	-	50	
			f=10kHz	-	20	
Output Voltage Noise	e _N	f=10Hz~100kHz I _O =10mA	C _O =2.2μF	-	30	μVrms
EN Input Threshold	V _{EH}	V _{IN} =2.7V to 7V	2.0	-	V _{IN}	V
	V _{EL}	V _{IN} =2.7V to 7V	0	-	0.4	V
EN Input Bias Current	I _{EH}	V _{EN} =V _{IN} , V _{IN} =2.7V to 7V	-	-	0.1	μA
	I _{EL}	V _{EN} =0V, V _{IN} =2.7V to 7V	-	-	0.5	μA
Shutdown Supply Current	I _{SD}	V _{IN} =5V, V _O =0V, V _{EN} <V _{EL}	-	0.5	1	μA
Shutdown Output Voltage	V _{O,SD}	I _O =0.4mA, V _{EN} <V _{EL}	0	-	0.4	V

Note 1: V_{OUT(E)} =Effective Output Voltage (i.e. the output voltage when "V_{OUT(T)} + 2.0V" is provided at the V_{IN} pin while maintaining a certain I_{OUT} value).

2: V_{OUT(T)} =Specified Output Voltage

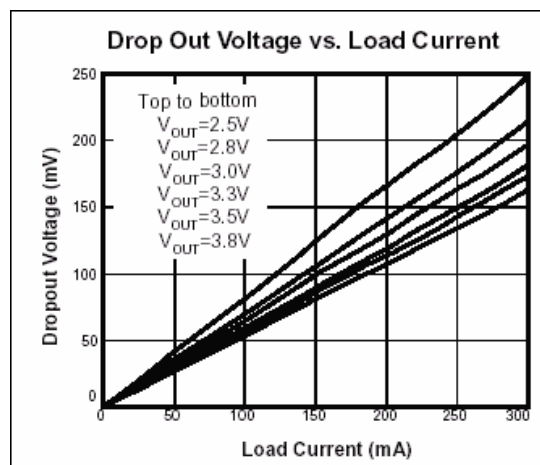
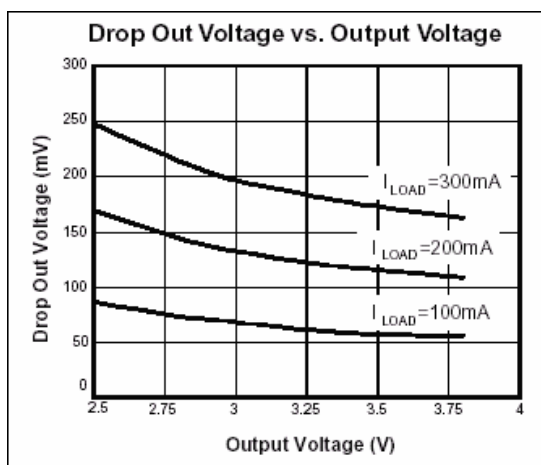
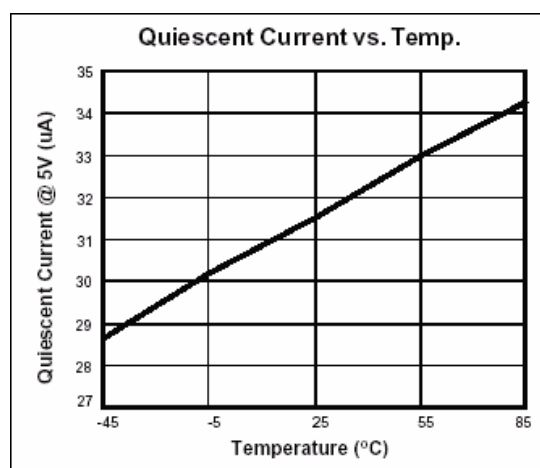
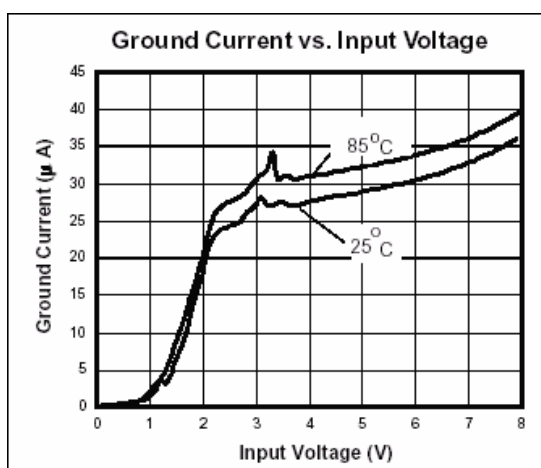
3: V_{IN(MIN)} =V_{OUT}+V_{DROPOUT}

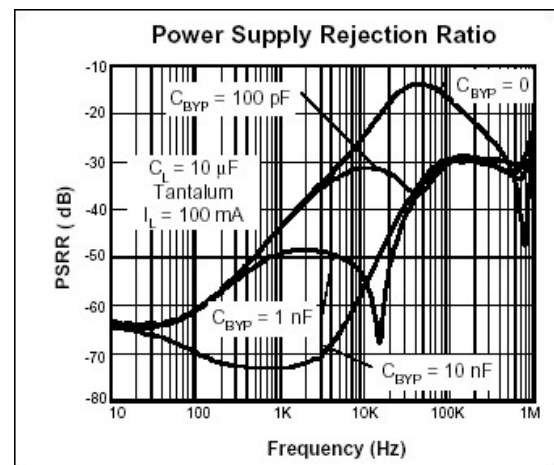
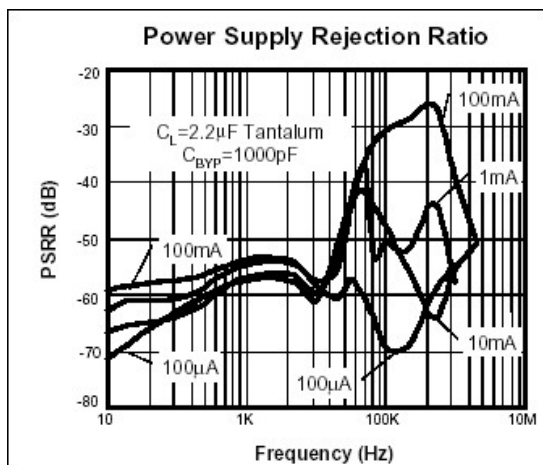
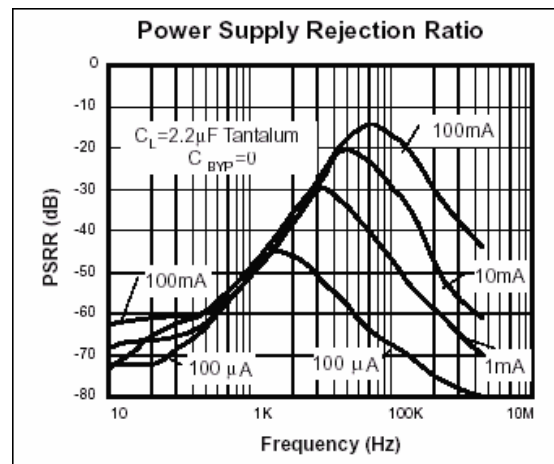
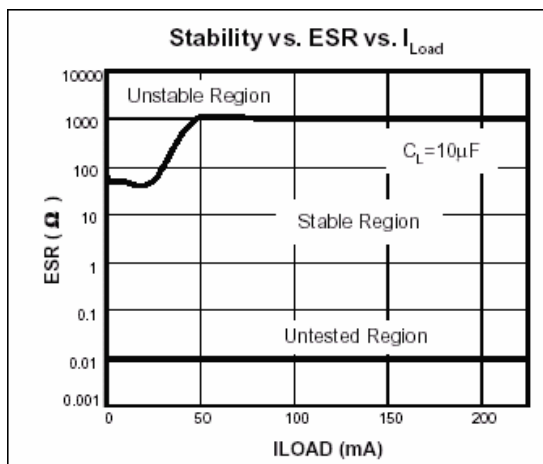
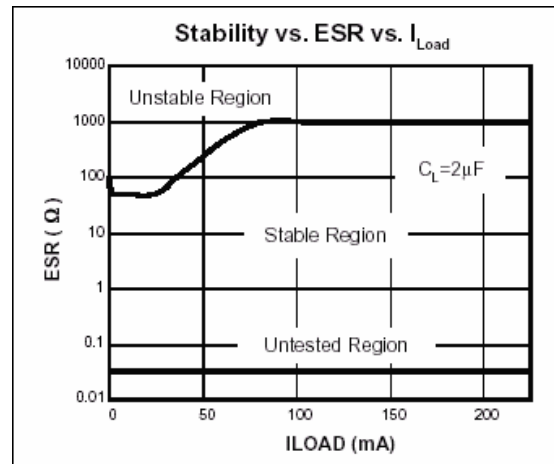
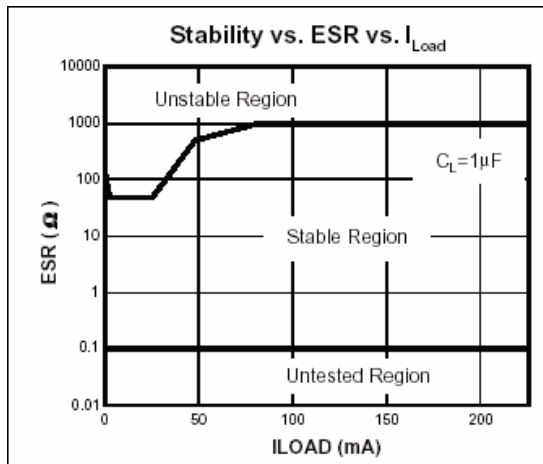
4: To prevent the Short Circuit Current protection feature from being prematurely activated, the input voltage must be applied before a current source load is applied.

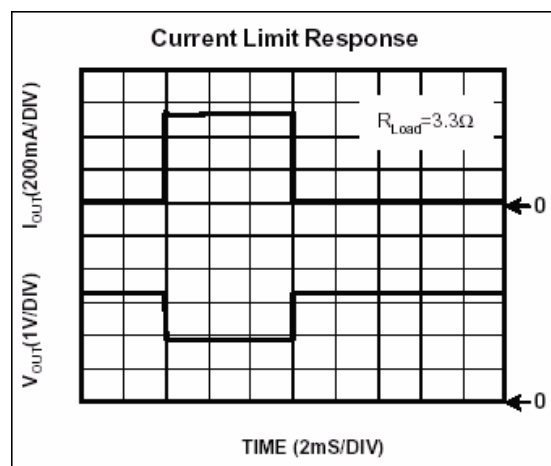
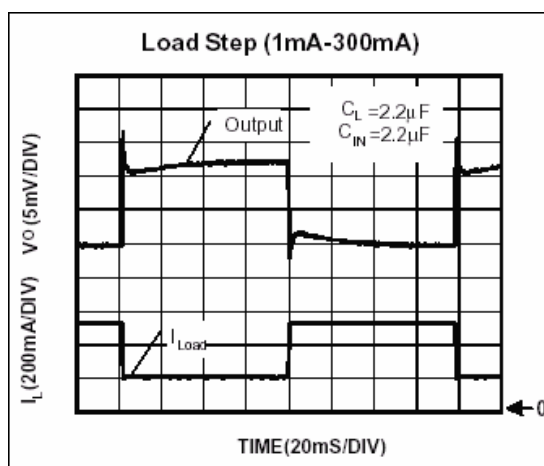
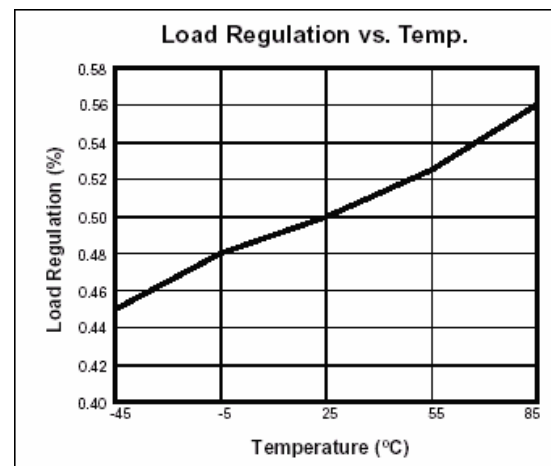
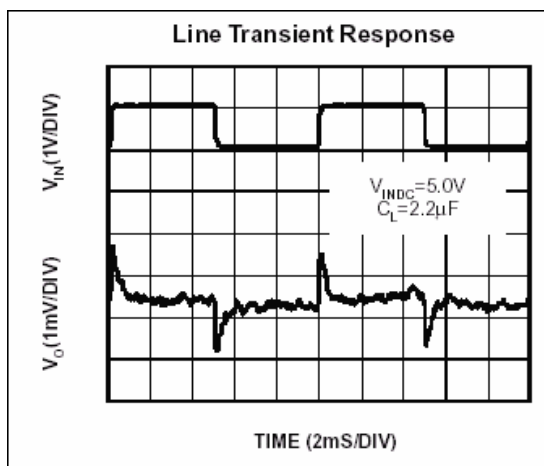
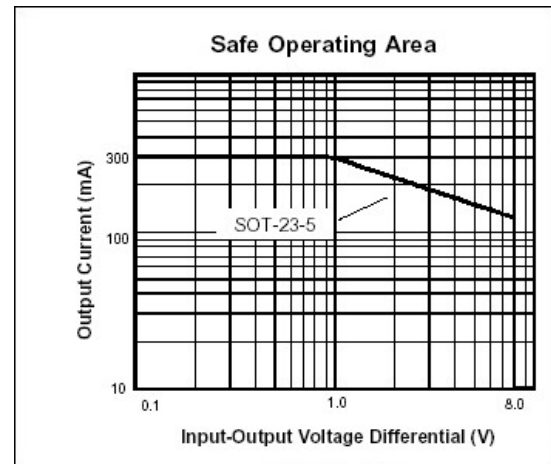
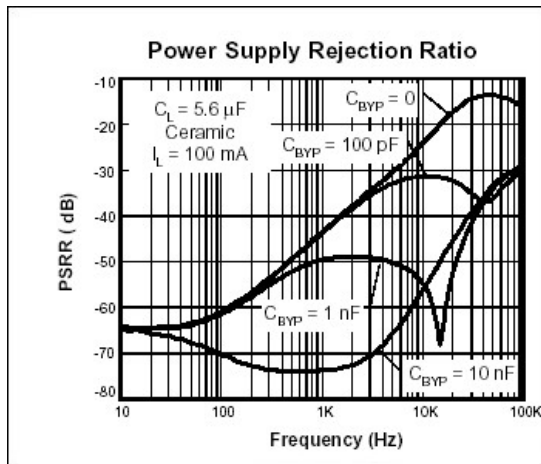
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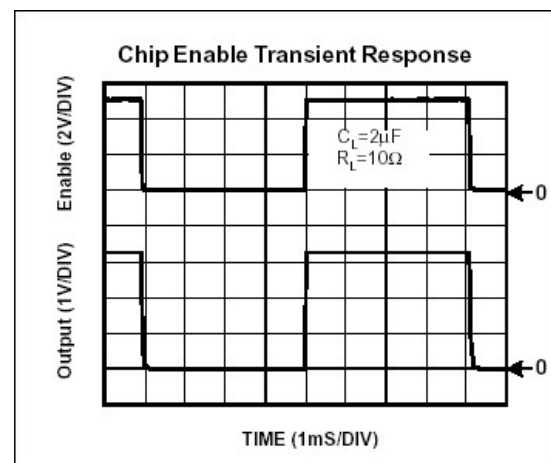
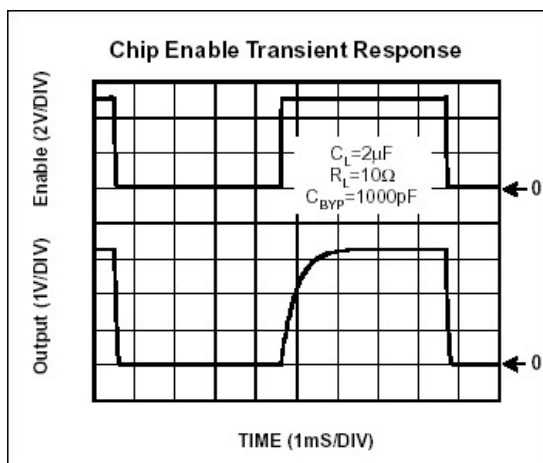
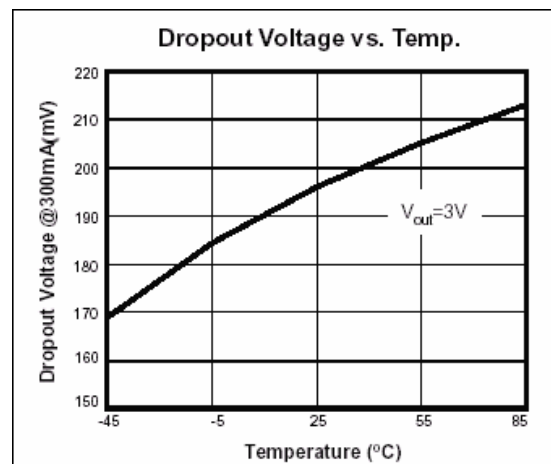
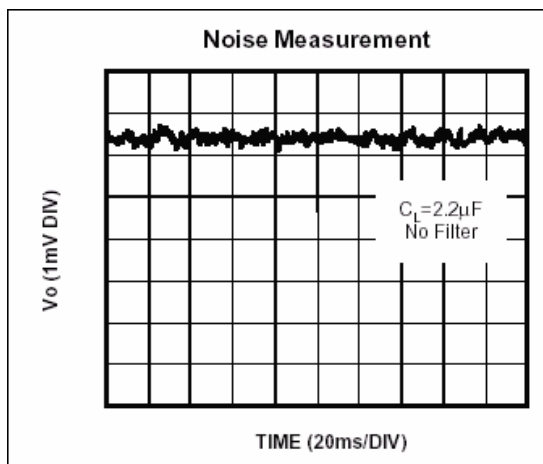
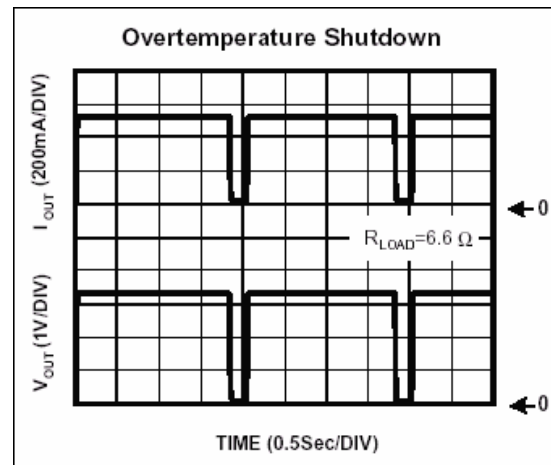
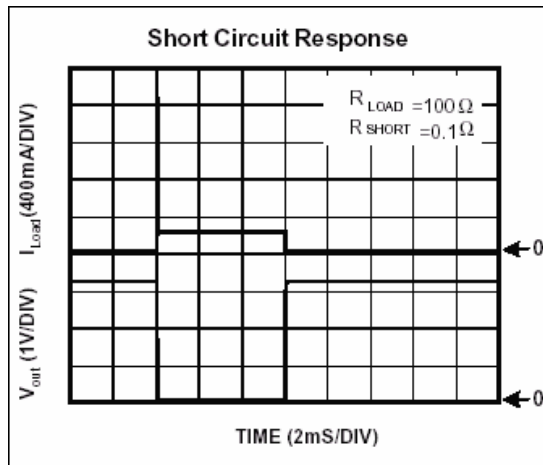
Part Number	Marking	Output Voltage	Part Number	Marking	Output Voltage
G5M2163-15	3F152 XXXX	1.5V	G5M2163-18	3F182 XXXX	1.8V
G5M2163-20	3F202 XXXX	2.0V	G5M2163-25	3F252 XXXX	2.5V
G5M2163-27	3F272 XXXX	2.7V	G5M2163-2H	3F2H2 XXXX	2.85V
G5M2163-28	3F282 XXXX	2.8V	G5M2163-29	3F292 XXXX	2.9V
G5M2163-30	3F302 XXXX	3.0V	G5M2163-31	3F312 XXXX	3.1V
G5M2163-33	3F332 XXXX	3.3V	G5M2163-34	3F342 XXXX	3.4V
G5M2163-35	3F352 XXXX	3.5V	G5M2163-36	3F362 XXXX	3.6V
G5M2163-37	3F372 XXXX	3.7V	G5M2163-38	3F382 XXXX	3.8V
G5M2163-50	3F502 XXXX	5.0V			

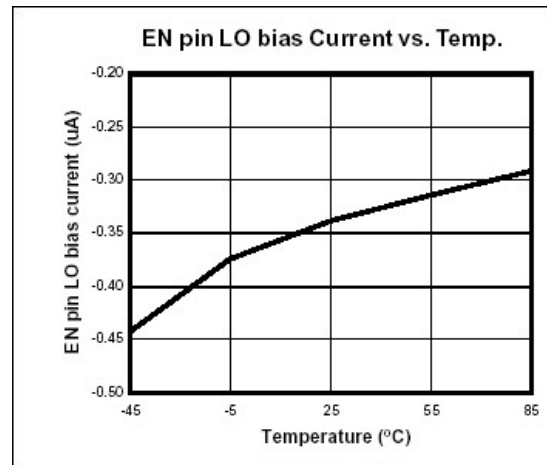
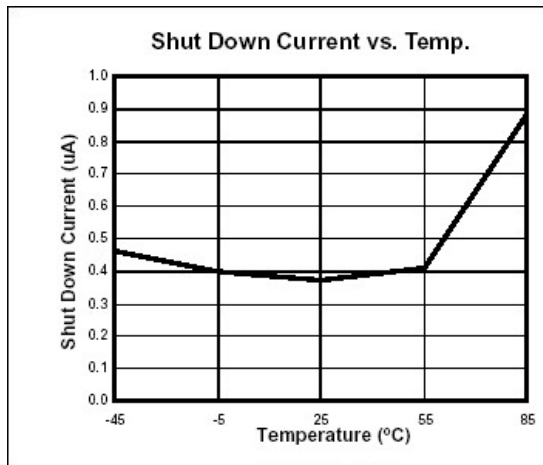
Characteristics Curve











Detailed Description

The G5M2163 series of COMS regulators contain a PMOS pass transistor, voltage reference, error amplifier, over-current protection, and thermal shutdown.

The P-channel pass transistor receives data from the error amplifier, over-current shutdown, and thermal protection circuits. During normal operation, the error amplifier compares the output voltage to a precision reference. Over-current and Thermal shutdown circuits become active when the junction temperature exceeds 150°C, or the current exceeds 300mA. During thermal shutdown, the output voltage remains low. Normal operation is restored when the junction temperature drops below 120°C.

The G5M2163 switches from voltage mode to current mode when the load exceeds the rated output current. This prevents over-stress. The G5M2163 also incorporates current fold-back to reduce power dissipation when the output is short circuited. This feature becomes active when the output drops below 0.8 volts, and reduces the current flow by 65%. Full current is restored when the voltage exceeds 0.8 volts.

External Capacitors

The G5M2163 is stable with an output capacitance to ground of 2.2μF or greater. Ceramic capacitors have the lowest ESR, and will offer the best AC performance. Conversely, Aluminum Electrolytic capacitors exhibit the highest ESR, resulting in the poorest AC response. Unfortunately, large value ceramic capacitors are comparatively expensive. One option is to parallel a 0.1μF ceramic capacitor with a 10μF Aluminum Electrolytic. The benefit is low ESR, high capacitance, and low overall cost.

A second capacitor is recommended between the input and ground to stabilize Vin. The input capacitor should be at least 0.1μF to have a beneficial effect.

A third capacitor can be connected between the BY-PASS pin and GND. This capacitor can be a low cost Polyester Film variety between the value of 0.001~0.01μF. A large capacitor improves the AC ripple rejection, but also makes the output come up slowly. This "Soft" turn-on is desirable in some applications to limit turn-on surges.

All capacitors should be placed in close proximity to the pins. A "Quiet" ground termination is desirable. This can be achieved with a "Star" connection.

Enable

The Enable pin normally floats high. When actively, pulled low, the PMOS pass transistor shuts off, and all internal circuits are powered down. In this state, the quiescent current is less than 1μA. This pin behaves much like an electronic switch.

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