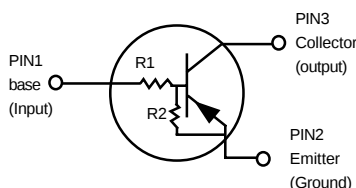


Bias Resistor Transistor

PNP Silicon Surface Mount Transistor with Monolithic Bias Resistor Network

This new series of digital transistors is designed to replace a single device and its external resistor bias network. The BRT (Bias Resistor Transistor) contains a single transistor with a monolithic bias network consisting of two resistors; a series base resistor and a base-emitter resistor. The BRT eliminates these individual components by integrating them into a single device. The use of a BRT can reduce both system cost and board space. The device is housed in the SOT-23 package which is designed for low power surface mount applications.

- * Simplifies Circuit Design
- * Reduces Board Space
- * Reduces Component Count
- * The SOT-23 package can be soldered using wave or reflow. The modified gull-winged leads absorb thermal stress during soldering eliminating the possibility of damage to the die.
- * Available in 8 mm embossed tape and reel. Use the Device Number to order the 7 inch/3000 unit reel. Replace "T1" with "T3" in the Device Number to order the 13 inch/10,000 unit reel



MMUN2111RLT1
MMUN2112RLT1
MMUN2113RLT1
MMUN2114RLT1
MMUN2115RLT1
MMUN2116RLT1
MMUN2130RLT1
MMUN2131RLT1
MMUN2132RLT1
MMUN2133RLT1
MMUN2134RLT1

PNP SILICON
BIAS RESISTOR
TRANSISTOR



CASE 318-08, STYLE 6
SOT- 23 (TO-236AB)

MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Rating	Symbol	Value	Unit
Collector-Base Voltage	V_{CBO}	50	Vdc
Collector-Emitter Voltage	V_{CEO}	50	Vdc
Collector Current	I_C	100	mAdc
Total Power Dissipation @ $T_A = 25^\circ\text{C}$ ⁽¹⁾	P_D	200	mW
Derate above 25°C		1.6	mW/ $^\circ\text{C}$

THERMAL CHARACTERISTICS

Rating	Symbol	Value	Unit
Thermal Resistance — Junction-to-Ambient (surface mounted)	$R_{\theta JA}$	625	$^\circ\text{C/W}$
Operating and Storage Temperature Range	T_J, T_{stg}	-65 to +150	$^\circ\text{C}$
Maximum Temperature for Soldering Purposes Time in Solder Bath	T_L	260 10	$^\circ\text{C}$ Sec

DEVICE MARKING AND RESISTOR VALUES

Device	Marking	R1 (K)	R2 (K)
MMUN2111LT1	A6A	10	10
MMUN2112LT1	A6B	22	22
MMUN2113LT1	A6C	47	47
MMUN2114LT1	A6D	10	47
MMUN2115LT1 ⁽²⁾	A6E	10	∞

1. Device mounted on a FR-4 glass epoxy printed circuit board using the minimum recommended footprint.
2. New devices. Updated curves to follow in subsequent data sheets.

MMUN2111RLT1 SERIES
DEVICE MARKING AND RESISTOR VALUES (Continued)

Device	Marking	R1 (K)	R2 (K)
MMUN2116RLT1 ⁽²⁾	A6F	4.7	∞
MMUN2130RLT1 ⁽²⁾	A6G	1.0	1.0
MMUN2131RLT1 ⁽²⁾	A6H	2.2	2.2
MMUN2132RLT1 ⁽²⁾	A6J	4.7	4.7
MMUN2133RLT1 ⁽²⁾	A6K	4.7	47
MMUN2134RLT1 ⁽²⁾	A6L	22	47

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
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OFF CHARACTERISTICS

Collector-Base Cutoff Current ($V_{CB}=50V$, $I_E = 0$)		I_{CBO}	-	-	100	nAdc
Collector-Emitter Cutoff Current ($V_{CE} = 50\text{ V}$, $I_B = 0$)		I_{CEO}	-	-	500	nAdc
Emitter-Base Cutoff Current ($V_{EB} = 6.0\text{ V}$, $I_C = 0$)	MMUN2111RLT1	I_{EBO}	-	-	0.5	mAdc
	MMUN2112RLT1		-	-	0.2	
	MMUN2113RLT1		-	-	0.1	
	MMUN2114RLT1		-	-	0.2	
	MMUN2115RLT1		-	-	0.9	
	MMUN2116RLT1		-	-	1.9	
	MMUN2130RLT1		-	-	4.3	
	MMUN2131RLT1		-	-	2.3	
	MMUN2132RLT1		-	-	1.5	
	MMUN2133RLT1		-	-	0.18	
MMUN2134RLT1		-	-	0.13		
Collector-Base Breakdown Voltage ($I_C = 10\text{ }\mu A$, $I_E = 0$)		$V_{(BR)CBO}$	50	-	-	Vdc
Collector-Emitter Breakdown Voltage ⁽³⁾ ($I_C=2.0mA$, $I_B=0$)		$V_{(BR)CEO}$	50	-	-	Vdc

ON CHARACTERISTICS ⁽³⁾

DC Current Gain ($V_{CE} = 10\text{V}$, $I_C = 5.0\text{mA}$)	MMUN2111RLT1	h_{FE}	35	60	-	
	MMUN2112RLT1		60	100	-	
	MMUN2113RLT1		80	140	-	
	MMUN2114RLT1		80	140	-	
	MMUN2115RLT1		160	250	-	
	MMUN2116RLT1		160	250	-	
	MMUN2130RLT1		3.0	5.0	-	
	MMUN2131RLT1		8.0	15	-	
	MMUN2132RLT1		15	27	-	
	MMUN2133RLT1		80	140	-	
	MMUN2134RLT1		80	130	-	
Collector-Emitter Saturation Voltage ($I_C=10\text{mA}$, $I_E=0.3\text{mA}$)	$V_{CE(sat)}$	-	-	0.25	Vdc	
($I_C = 10\text{mA}$, $I_B = 5\text{mA}$)	MMUN2130RLT1	MMUN2131RLT1				
($I_C = 10\text{mA}$, $I_B = 1\text{mA}$)	MMUN2115RLT1	MMUN2116RLT1				
	MMUN2132RLT1	MMUN2133RLT1	MMUN2134RLT1			
Output Voltage (on)	V_{OL}				Vdc	
(V _{CC} =5.0V, V _B =2.5V, R _L =1.0k Ω)	MMUN2111RLT1	-	-	0.2		
	MMUN2112RLT1	-	-	0.2		
	MMUN2114RLT1	-	-	0.2		
	MMUN2115RLT1	-	-	0.2		
	MMUN2116RLT1	-	-	0.2		
	MMUN2130RLT1	-	-	0.2		
	MMUN2131RLT1	-	-	0.2		
	MMUN2132RLT1	-	-	0.2		
	MMUN2133RLT1	-	-	0.2		
	MMUN2134RLT1	-	-	0.2		
(V _{CC} =5.0V, V _B =3.5V, R _L = 1.0k Ω)	MMUN2113RLT1	-	-	0.2		

2. New devices. Updated curves to follow in subsequent data sheets.

3. Pulse Test: Pulse Width < 300 ms, Duty Cycle < 2.0%

2-446 LRC Small-Signal Transistors, FETs and Diodes Device Data

MMUN2111RLT1 SERIES

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted) (Continued)

Characteristic	Symbol	Min	Typ	Max	Unit
Output Voltage (off) ($V_{CC} = 5.0\text{V}$, $V_B = 0.5\text{V}$, $R_L = 1.0\text{k}\Omega$) ($V_{CC} = 5.0\text{V}$, $V_B = 0.25\text{V}$, $R_L = 1.0\text{k}\Omega$) MMUN2115RLT1 MMUN2116RLT1 MMUN2131RLT1 MMUN2132RLT1 ($V_{CC} = 5.0\text{V}$, $V_B = 0.050\text{V}$, $R_L = 1.0\text{k}\Omega$) MMUN2130RLT1	V_{OH}	4.9	—	—	Vdc
Input Resistor MMUN2111RLT1 MMUN2112RLT1 MMUN2113RLT1 MMUN2114RLT1 MMUN2115RLT1 MMUN2116RLT1 MMUN2130RLT1 MMUN2131RLT1 MMUN2132RLT1 MMUN2133RLT1 MMUN2134RLT1	R_1	7.0 15.4 32.9 7.0 7.0 3.3 0.7 1.5 3.3 3.3 15.4	10 22 47 10 10 4.7 1.0 2.2 4.7 4.7 22	13 28.6 61.1 13 13 6.1 1.3 2.9 6.1 6.1 28.6	k Ω
Resistor Ratio MMUN2111RLT1 MMUN2112RLT1 MMUN2113RLT1 MMUN2114RLT1 MMUN2115RLT1 MMUN2116RLT1 MMUN2130RLT1 MMUN2131RLT1 MMUN2132RLT1 MMUN2133RLT1	R_1/R_2	0.8 0.17 — 0.8 0.055	1.0 0.21 — 1.0 0.1	1.2 0.25 — 1.2 0.185	

TYPICAL ELECTRICAL CHARACTERISTICS
MMUN2111RLT1

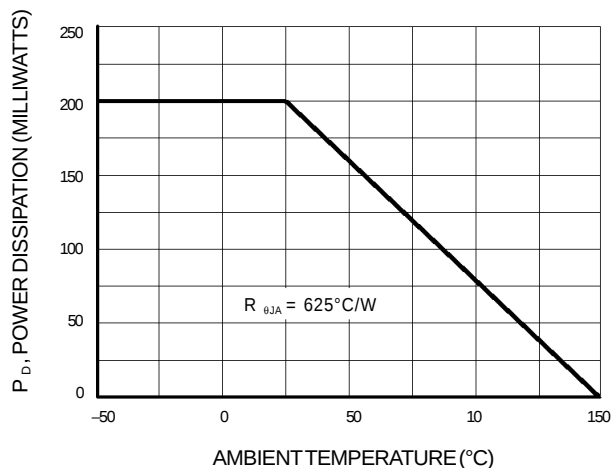


Figure 1. Derating Curve

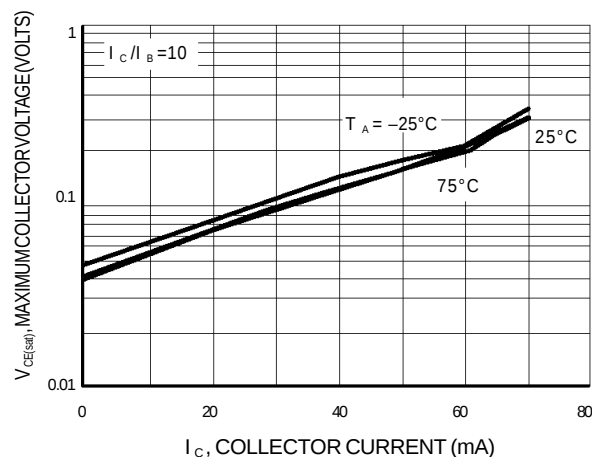


Figure 2. $V_{CE(sat)}$ versus I_C

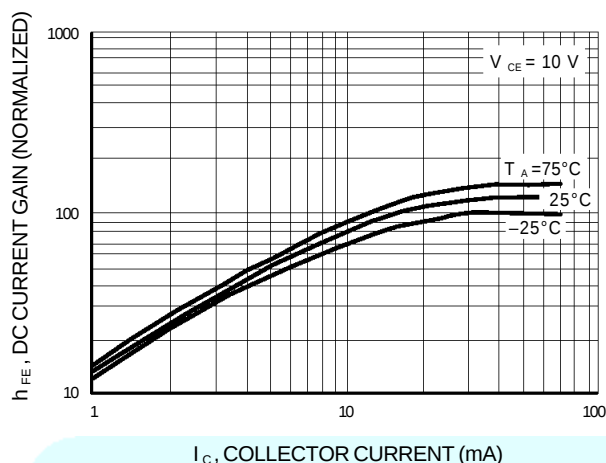


Figure 3. DC Current Gain

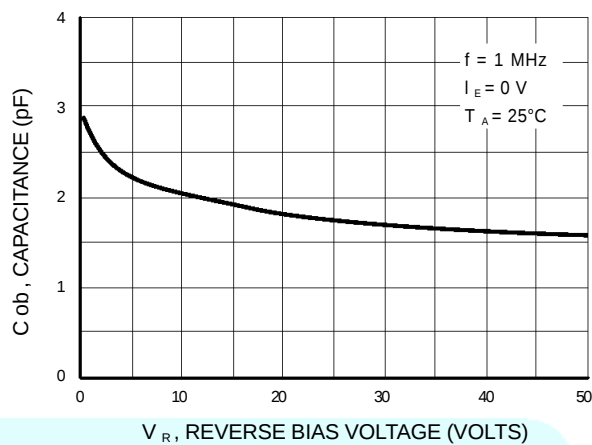


Figure 4. Output Capacitance

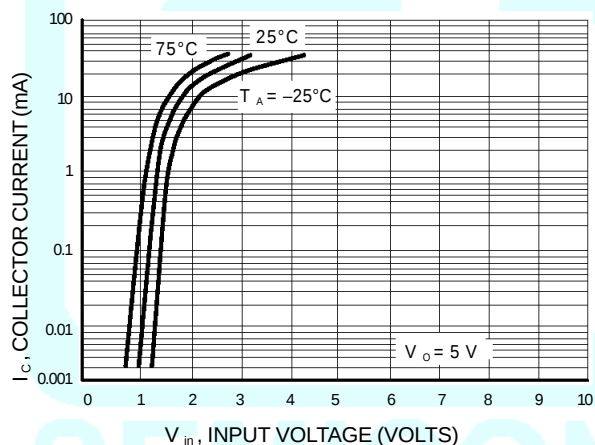


Figure 5. Output Current versus Input Voltage

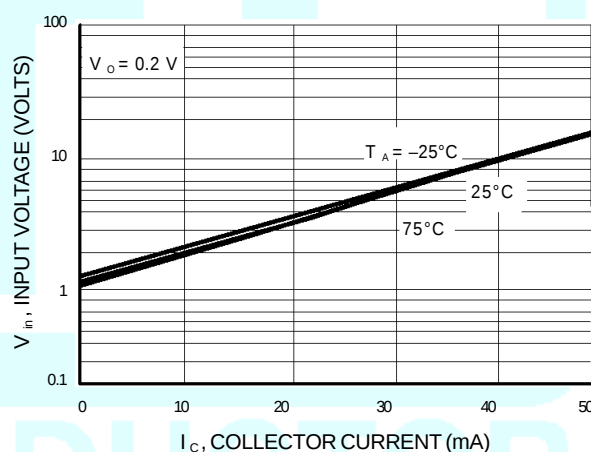


Figure 6. Input Voltage versus Output Current

MMUN2111RLT1 SERIES

TYPICAL ELECTRICAL CHARACTERISTICS

MMUN2112RLT1

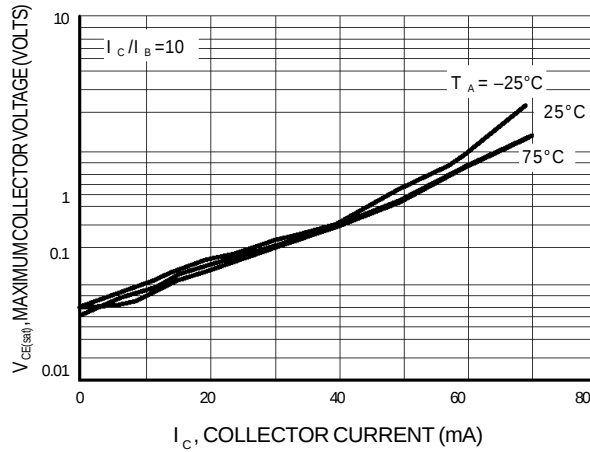


Figure 7. $V_{CE(sat)}$ versus I_C

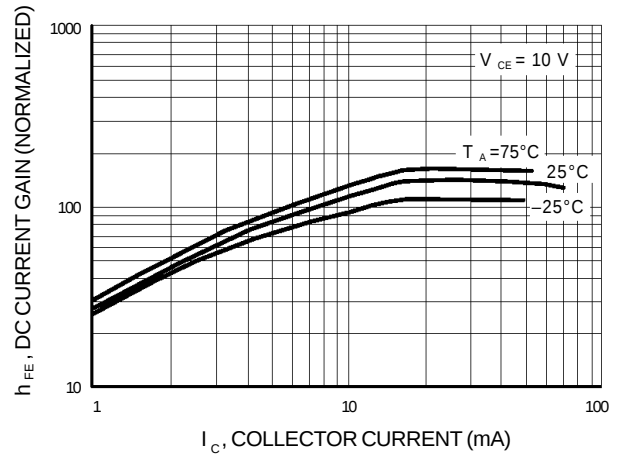


Figure 8. DC Current Gain

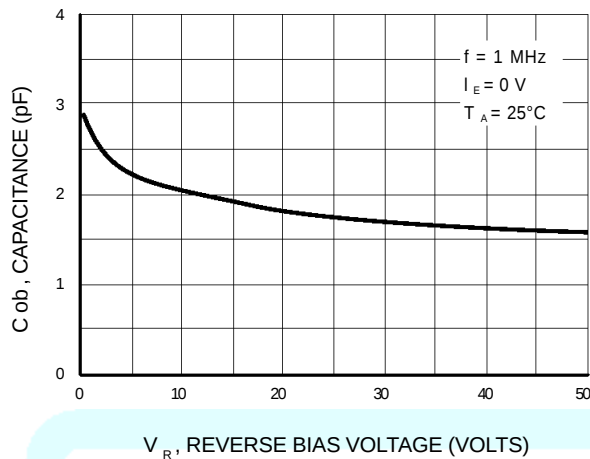


Figure 9. Output Capacitance

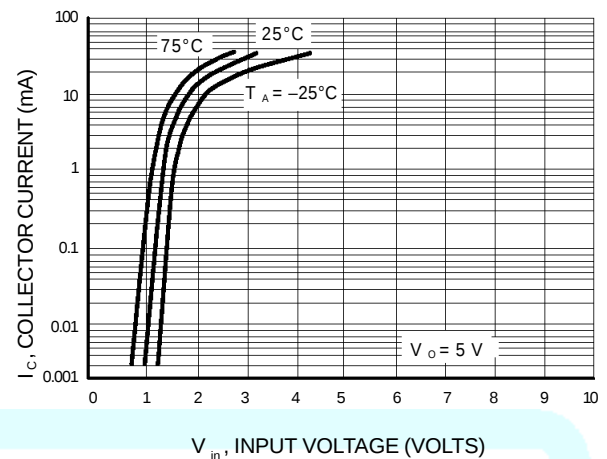


Figure 10. Output Current versus Input Voltage

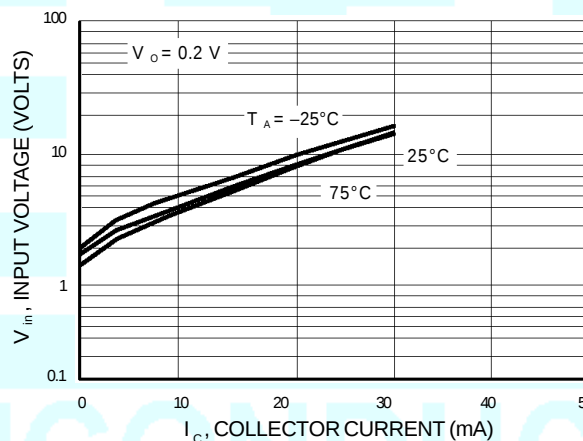


Figure 11. Input Voltage versus Output Current

TYPICAL ELECTRICAL CHARACTERISTICS

MMUN2113RLT1

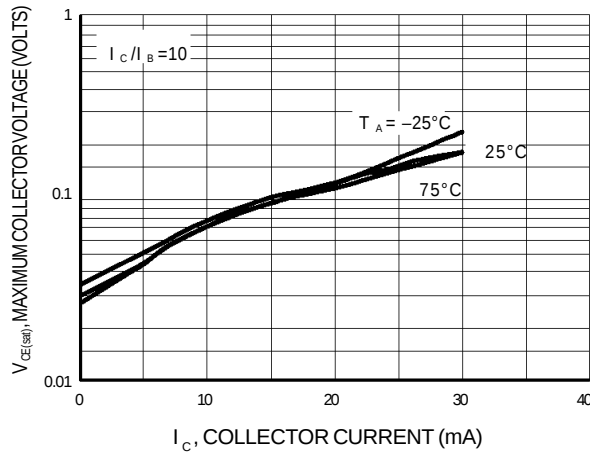


Figure 12. $V_{CE(sat)}$ versus I_C

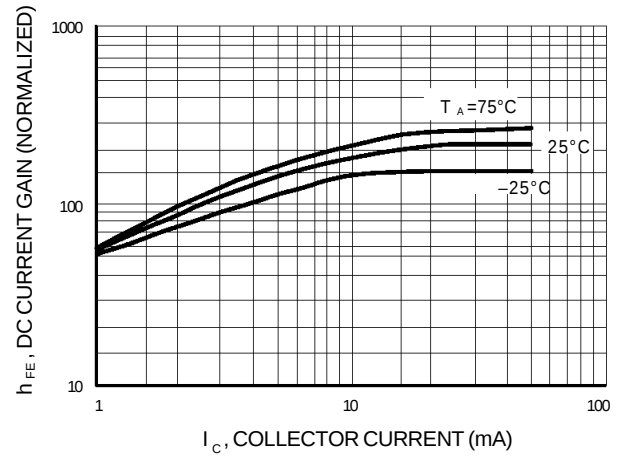


Figure 13. DC Current Gain

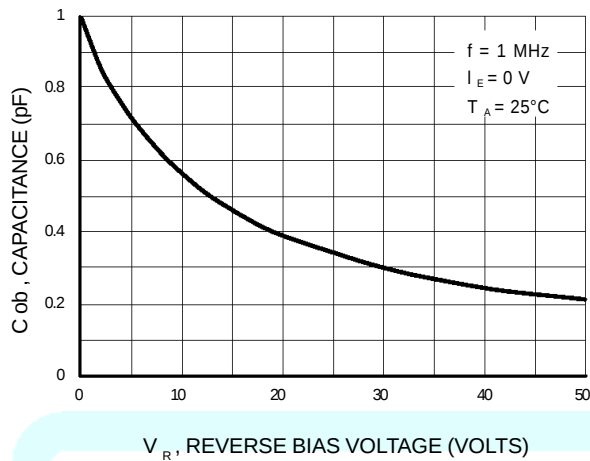


Figure 14. Output Capacitance

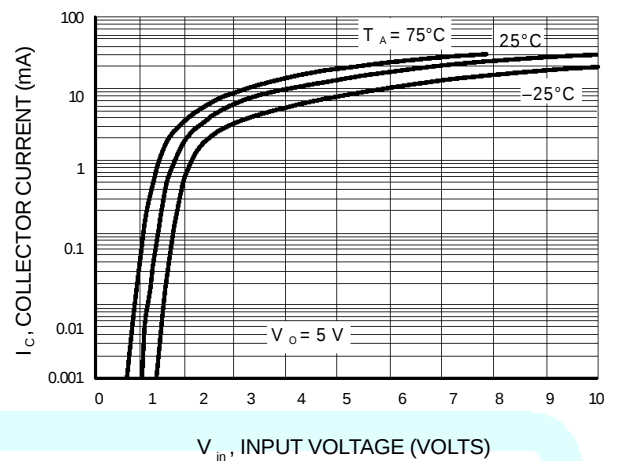


Figure 15. Output Current versus Input Voltage

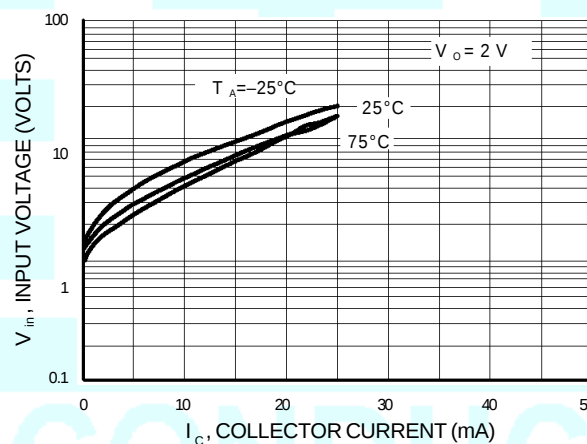


Figure 16. Input Voltage versus Output Current

TYPICAL ELECTRICAL CHARACTERISTICS
MMUN2114RLT1

