

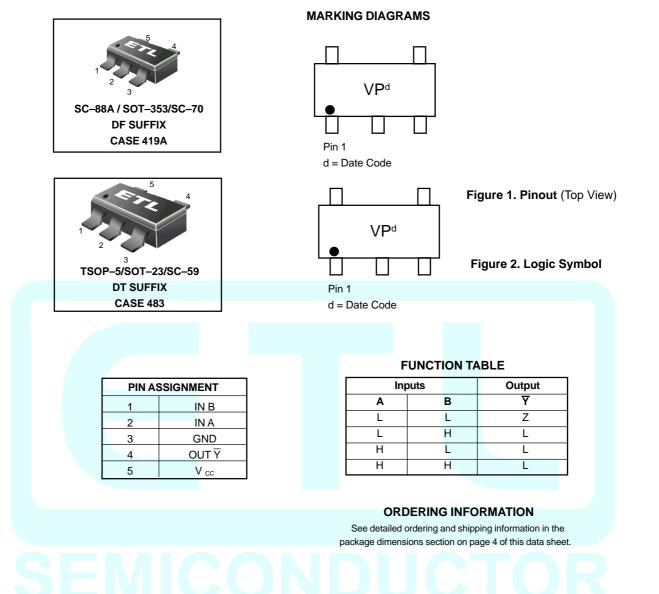
# 2–Input NAND Gate with Open Drain Output



The MC74VHC1G03 is an advanced high speed CMOS 2–input NOR gate with an open drain output fabricated with silicon gate CMOS technology. It achieves high speed operation similar to equivalent Bipolar Schottky TTL while maintaining CMOS low power dissipation.

The internal circuit is composed of three stages, including an open drain output which provides the capability to set output switching level. This allows the MC74VHC1G03 to be used to interface 5 V circuits to circuits of any voltage between V  $_{cc}$  and 7 V using an external resistor and power supply.

- The MC74VHC1G03 input structure provides protection when voltages up to 7 V are applied, regardless of the supply voltage.
- High Speed: t  $_{\text{PD}}$  = 3.6 ns (Typ) at V  $_{\text{CC}}$  = 5 V
- Low Internal Power Dissipation: I  $_{CC}$  = 2 mA (Max) at T  $_{A}$  = 25°C
- Power Down Protection Provided on Inputs
- Pin and Function Compatible with Other Standard Logic Families
- Chip Complexity: FETs = 62; Equivalent Gates = 16





## MC74VHC1G03

#### **MAXIMUM RATINGS**

Symbol	Parameter		Value	Unit
V <sub>cc</sub>	DC Supply Voltage		- 0.5 to + 7.0	V
V IN	DC Input Voltage		- 0.5 to 7.0	V
V <sub>OUT</sub>	DC Output Voltage	V <sub>cc</sub> =0	- 0.5 to 7.0	V
		High or Low State	–0.5 to V cc + 0.5	
I <sub>IK</sub>	Input Diode Current		-20	mA
I <sub>ок</sub>	Output Diode Current	V <sub>out</sub> < GND; V <sub>out</sub> > V <sub>cc</sub>	+20	mA
I <sub>OUT</sub>	DC Output Current, per Pin		+ 25	mA
I <sub>cc</sub>	DC Supply Current, V $_{\rm CC}$ and GND		+50	mA
PD	Power dissipation in still air	SC–88A, TSOP–5	200	mW
θ <sub>JA</sub>	Thermal resistance	SC–88A, TSOP–5	333	°C/W
T∟	Lead Temperature, 1 mm from Case f	for 10 s	260	°C
ΤJ	Junction Temperature Under Bias		+ 150	°C
T stg	Storage temperature		-65 to +150	°C
V <sub>ESD</sub>	ESD Withstand Voltage	Human Body Model (Note 2)	>2000	V
		Machine Model (Note 3)	> 200	
		Charged Device Model (Note 4)	N/A	
LATCH-UP	Latch–Up Performance Above V cc	and Below GND at 125°C (Note 5)	± 500	mA

 Maximum Ratings are those values beyond which damage to the device may occur. Exposure to these conditions or conditions beyond those indicated may adversely affect device reliability. Functional operation under absolute-maximum-rated conditions is not implied. Functional operation should be restricted to the Recommended Operating Conditions.

2. Tested to EIA/JESD22-A114-A

3. Tested to EIA/JESD22-A115-A

4. Tested to JESD22-C101-A

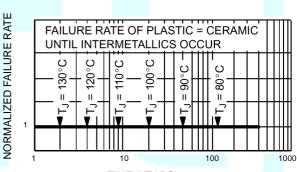
5. Tested to EIA/JESD78

#### **RECOMMENDED OPERATING CONDITIONS**

Symbol	Parameter	Min	Max	Unit	
V <sub>cc</sub>	DC Supply Voltage		2.0	5.5	V
V <sub>IN</sub>	DC Input Voltage		0.0	5.5	V
V <sub>OUT</sub>	DC Output Voltage		0.0	7.0	V
Τ <sub>A</sub>	Operating Temperature Range		- 55	+ 125	°C
t <sub>r</sub> ,t <sub>f</sub>	Input Rise and Fall Time	$V_{cc} = 3.3 \pm 0.3 V$	0	100	ns/V
		$V_{cc} = 5.0 \pm 0.5 V$	0	20	

#### DEVICE JUNCTION TEMPERATURE VERSUS TIME TO 0.1% BOND FAILURES

Junction	Time,	Time,
Temperature °C	Hours	Years
80	1,032,200	117.8
90	419,300	47.9
100	178,700	20.4
110	79,600	9.4
120	37,000	4.2
130	17,800	2.0
140	8,900	1.0



TIME, YEARS

Figure 3. Failure Rate vs. Time Junction Temperature



## MC74VHC1G03

### DC ELECTRICAL CHARACTERISTICS

			V <sub>cc</sub>	T <sub>A</sub> = 25°C		$T_A = 25^{\circ}C$ $T_A \le 85^{\circ}$			<b>85</b> °C –55°C≤T <sub>A</sub> ≤125°C		
Symbol	Parameter	<b>Test Conditions</b>	(V)	Min	Тур	Max	Min	Max	Min	Max	Unit
V IH	Minimum High–Level		2.0	1.5			1.5		1.5		V
	Input Voltage		3.0	2.1			2.1		2.1		
			4.5	3.15			3.15		3.15		
			5.5	3.85			3.85		3.85		
V IL	Maximum Low–Level		2.0			0.5		0.5		0.5	V
	Input Voltage		3.0			0.9		0.9		0.9	
			4.5			1.35		1.35		1.35	
			5.5			1.65		1.65		1.65	
V <sub>OH</sub>	Minimum High–Level	$V_{IN} = V_{IH} \text{ or } V_{IL}$	2.0	1.9	2.0		1.9		1.9		V
	Output Voltage	I <sub>он</sub> = – 50 µА	3.0	2.9	3.0		2.9		2.9		
	$V_{IN} = V_{IH} \text{ or } V_{IL}$		4.5	4.4	4.0		4.4		4.4		
		$V_{IN} = V_{IH} \text{ or } V_{IL}$									
		I <sub>он</sub> = –4 mA	3.0	2.58			2.48		2.34		
		I <sub>он</sub> = -8 mА	4.5	3.94			3.80		3.66		
V <sub>OL</sub>	Maximum Low-Level	$V_{IN} = V_{IH} \text{ or } V_{IL}$	2.0		0.0	0.1		0.1		0.1	V
	Output Voltage	I <sub>OL</sub> = 50 μA	3.0		0.0	0.1		0.1		0.1	
	$V_{IN} = V_{IH} \text{ or } V_{IL}$		4.5		0.0	0.1		0.1		0.1	
		$V_{IN} = V_{IH} \text{ or } V_{IL}$									
		$I_{OL} = 4 \text{ mA}$	3.0			0.36		0.44		0.52	
		I <sub>oL</sub> =8 mA	4.5			0.36		0.44		0.52	
I <sub>IN</sub>	Maximum Input	V $_{\rm IN}$ = 5.5 V or GND	0 to5.5			±0.1		±1.0		±1.0	μΑ
	Leakage Current										
I <sub>cc</sub>	Maximum Quiescent	$V_{IN} = V_{CC} \text{ or } GND$	5.5			2.0		20		40	μΑ
	Supply Current										
I OPD	Maximum Off-state	V <sub>OUT</sub> = 5.5 V	0			0.25		2.5		5.0	μΑ
	Leakage Current										

#### AC ELECTRICAL CHARACTERISTICS C $_{load}$ = 50 pF, Input t $_{r}$ = t $_{f}$ = 3.0 ns

			T <sub>A</sub> = 25°C		T <sub>A</sub> ≤ 85°C		<b>−55°C to 125°</b> C			
Symbol	Parameter	Test Conditions	Min	Тур	Max	Min	Max	Min	Max	Unit
t PZL	Maximum Output	V CC = 3.3 ± 0.3 V C L = 15 pF		5.6	7.9		9.5		11.0	ns
	Enable Time,	$R L = R I = 500 \Omega$ $C L = 50 pF$		8.1	11.4		13.0		15.5	
	Input A or B to Y									
		V CC = 5.0 ± 0.5 V C L = 15 pF		3.6	5.5		6.5		8.0	
		$R L = R I = 500 \Omega$ $C L = 50 pF$		5.1	7.5		8.5		10.0	
t PLZ	Maximum Output	V CC = 3.3 ± 0.3 V C L = 50 pF		8.1	11.4		13.0		15.5	ns
	Disable Time	R L = R I = 500 Ω								
		V CC = 5.0 ± 0.5 V C L = 50 pF		5.1	7.5		8.5		10.0	
		R L = R I = 500 Ω								
C IN	Maximum Input			4	10		10		10	pF
	Capacitance									

		Typical @ 25°C, V $_{cc}$ = 5.0 V	
C PD	Power Dissipation Capacitance (Note 6)	18	рF

6. C <sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation:  $I_{CC(OPR)} = C_{PD} X V_{CC} X f_{in} + I_{CC} C_{PD}$  is used to determine the no–load dynamic power consumption;  $P_{D} = C_{PD} X V_{CC}^2 X f_{in} + I_{CC} X V_{CC}$ .



## MC74VHC1G03

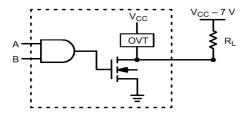


Figure 4. Output Voltage Mismatch Application

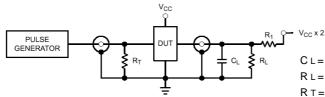


Figure 6. Test Circuit

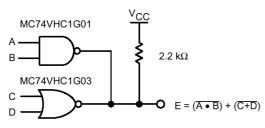


Figure 7. Complex Boolean Functions

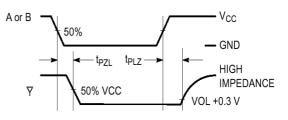
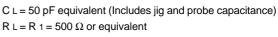
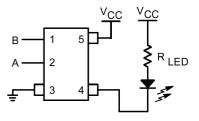


Figure 5. Switching Waveforms



R T = Z OUT of pulse generator (typically 50  $\Omega$ )



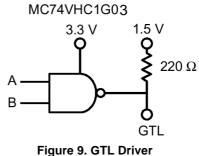


Figure 8. LED Driver

## **DEVICE ORDERING INFORMATION**

	Device Nomenclature										
Device Order Number	Logic Circuit Indicator	Temp Range Identifier	Technology	Device Function	Package Suffix	Tape and Reel Suffix	Package Type (Name/SOT#/ Common Name)	Tape and Reel Size			
MC74VHC1G03DFT	1 MC	74	VHC1G	03	DF	T1	SC-70/SC-88A/ SOT-353	178 mm (7 in) 3000 Unit			
MC74VHC1G03DFT	2 MC	74	VHC1G	03	DF	T2	SC-70/SC-88A/ SOT-353	178 mm (7 in) 3000 Unit			
MC74VHC1G03DFT	4 MC	74	VHC1G	03	DF	T4	SC-70/SC-88A/ SOT-353	330 mm (13 in) 10,000 Unit			
MC74VHC1G03DTT	1 MC	74	VHC1G	03	DT	T1	SOT-23/TSOPS/ SC-59	178 mm (7 in) 3000 Unit			
MC74VHC1G03DTT	3 MC	74	VHC1G	03	DT	Т3	SOT-23/TSOPS/ SC-59	330 mm (13 in) 10,000 Unit			